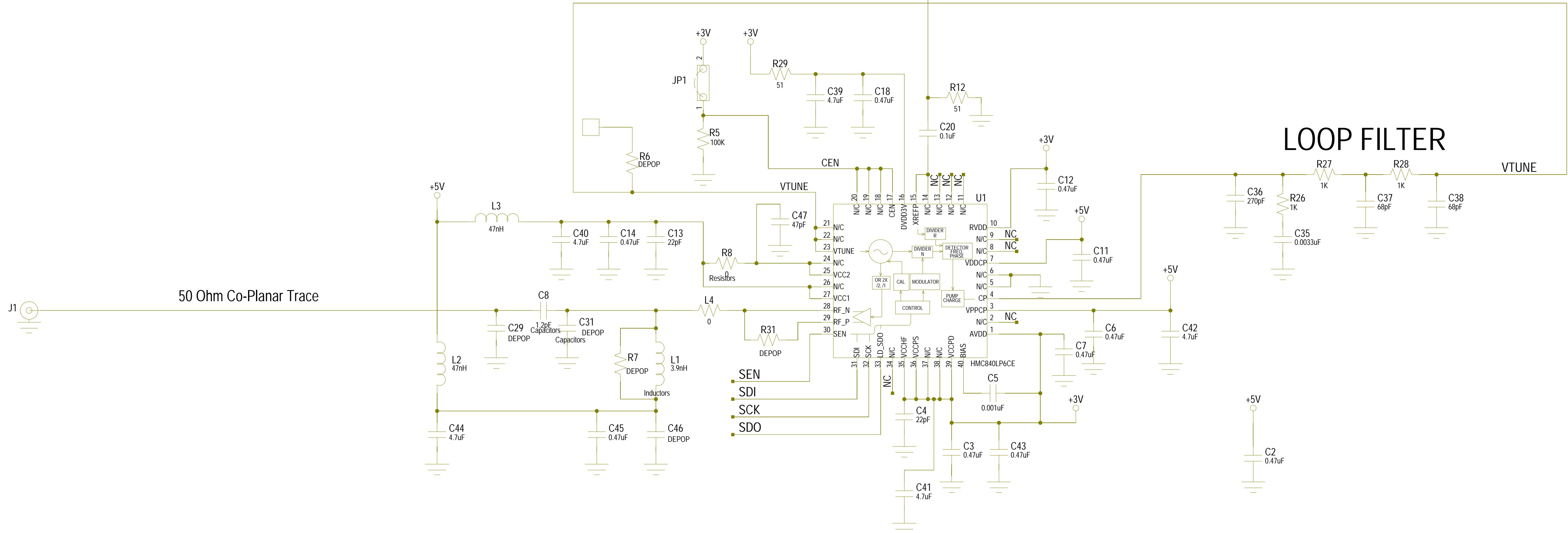
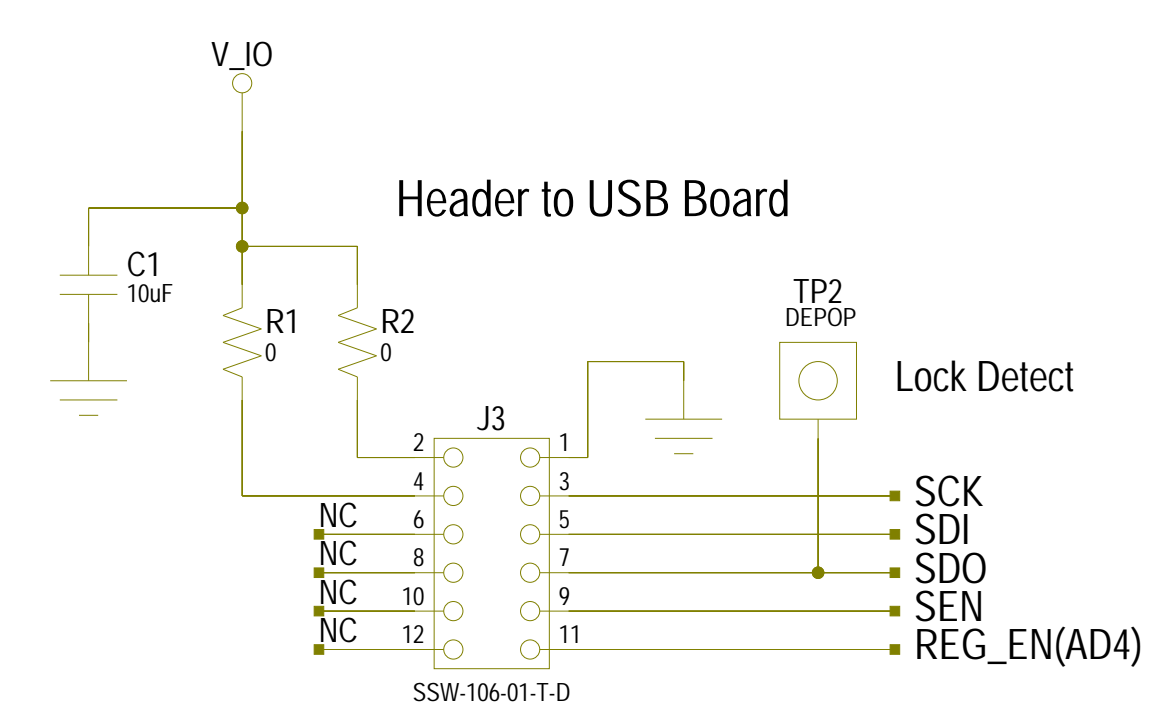
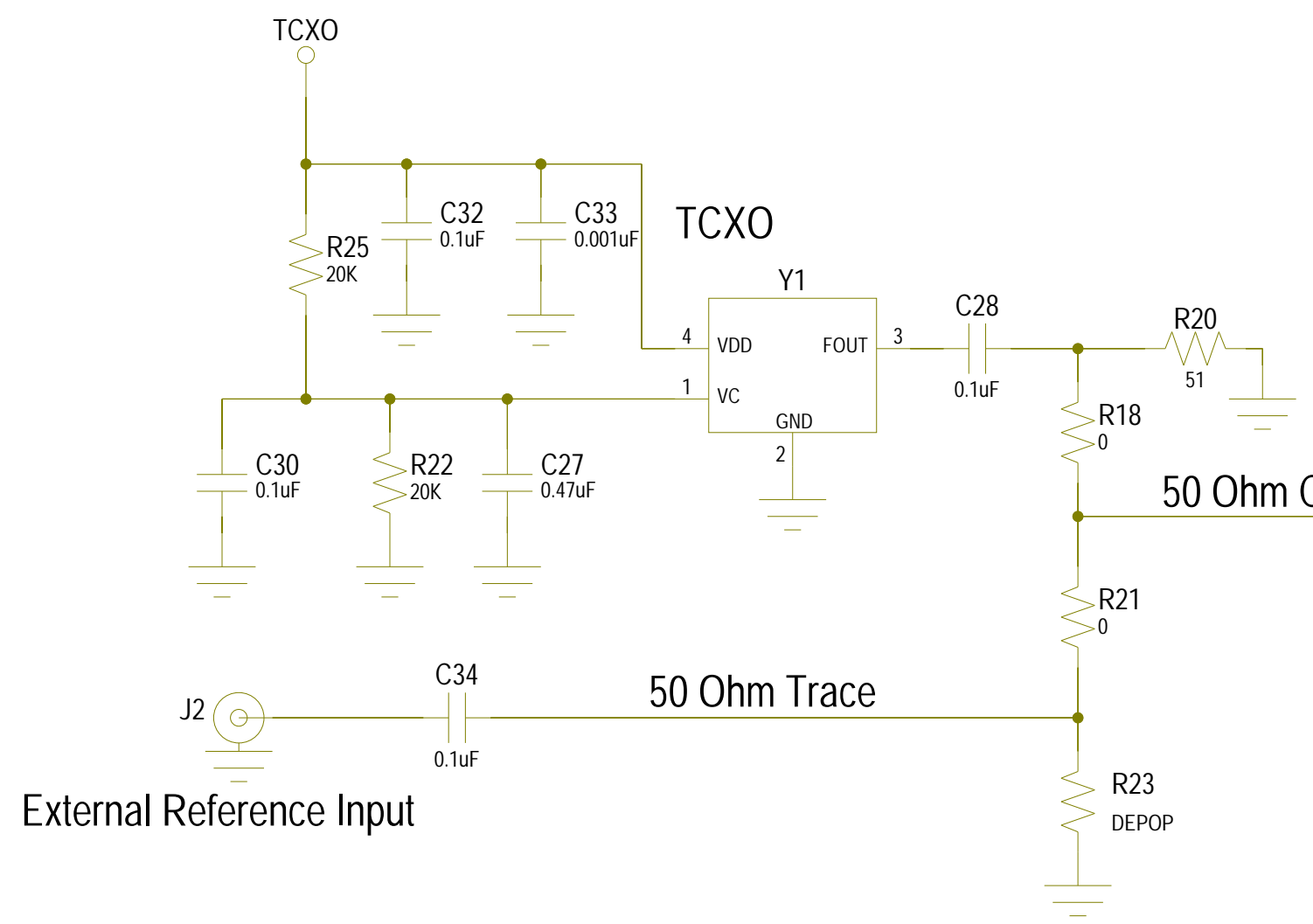
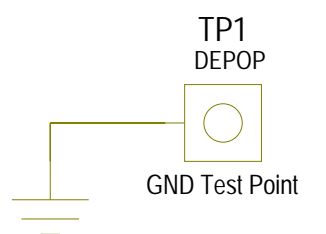


REVISIONS					
REV	ECN#	ZONE	DESCRIPTION	NAME	DATE
B	CP100519	----	RELEASED PRODUCTION ECN CP100519	V. VADUVA	12/29/2010
C	CP121275	----	CHANGE RELEASE PER CP121275	V. VADUVA	09/14/2012

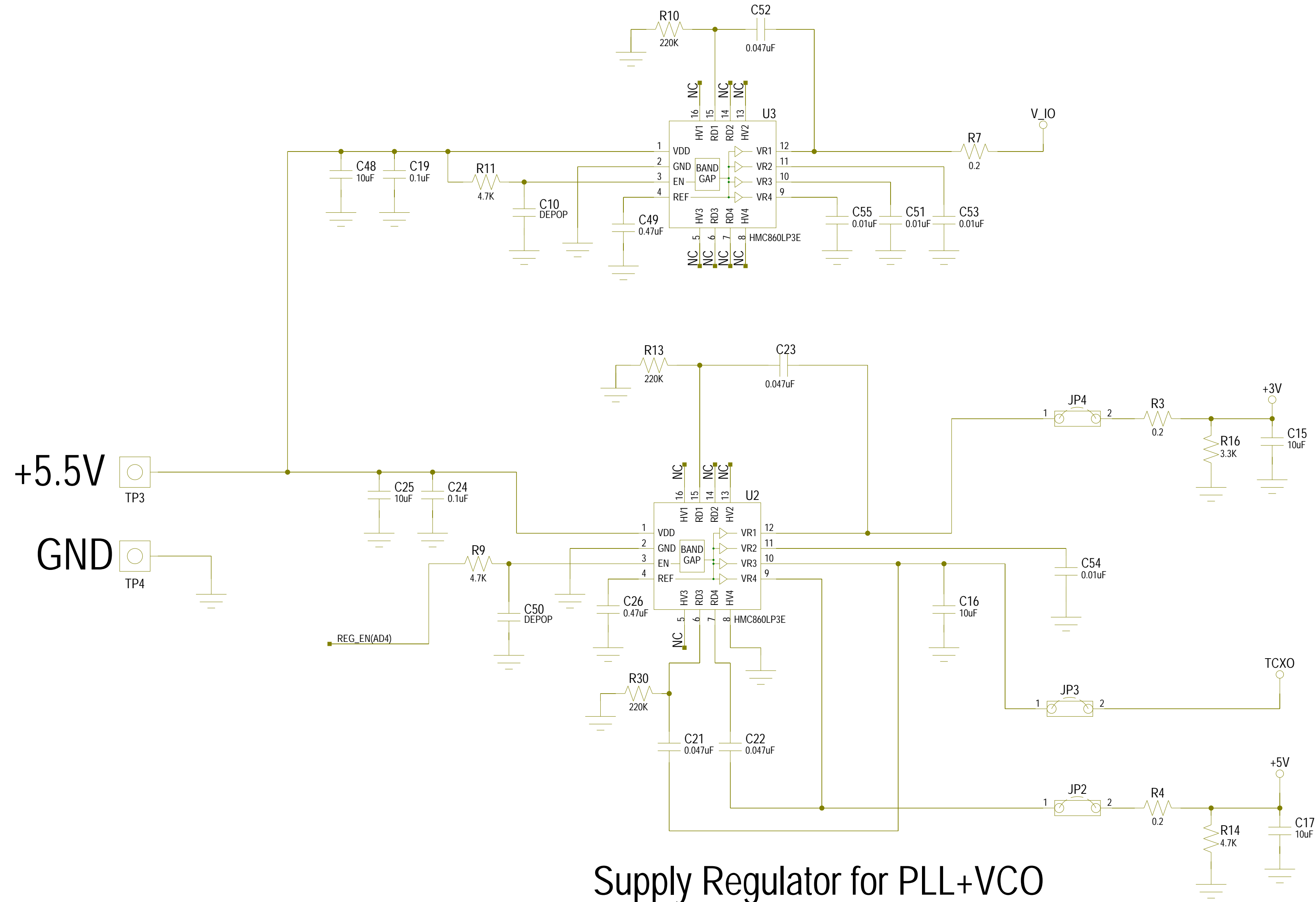


## HMC840 Fo & FO/2 Integrated PLL & VCO Eval



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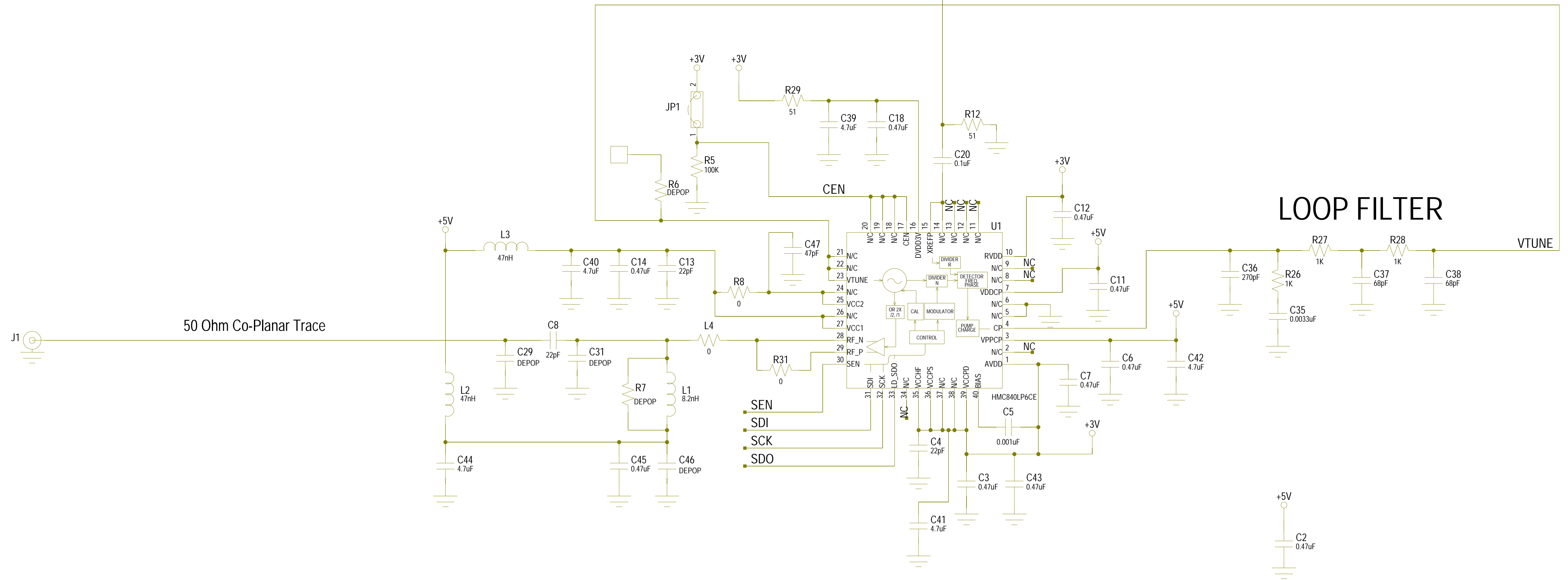
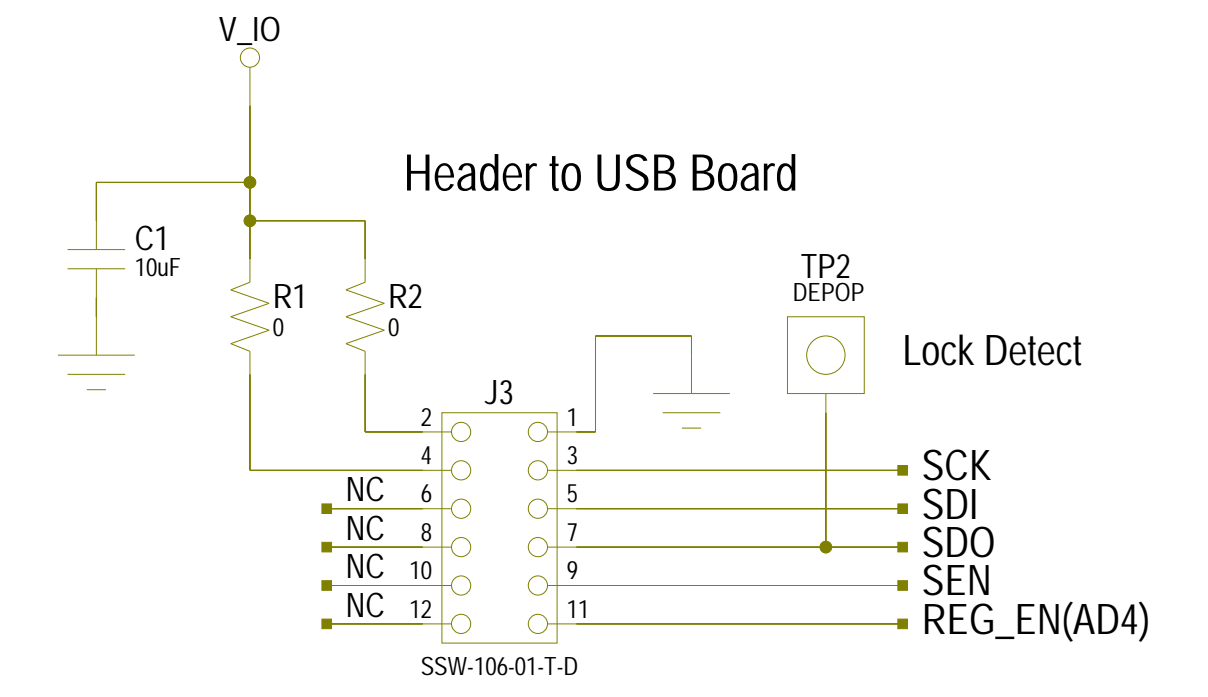
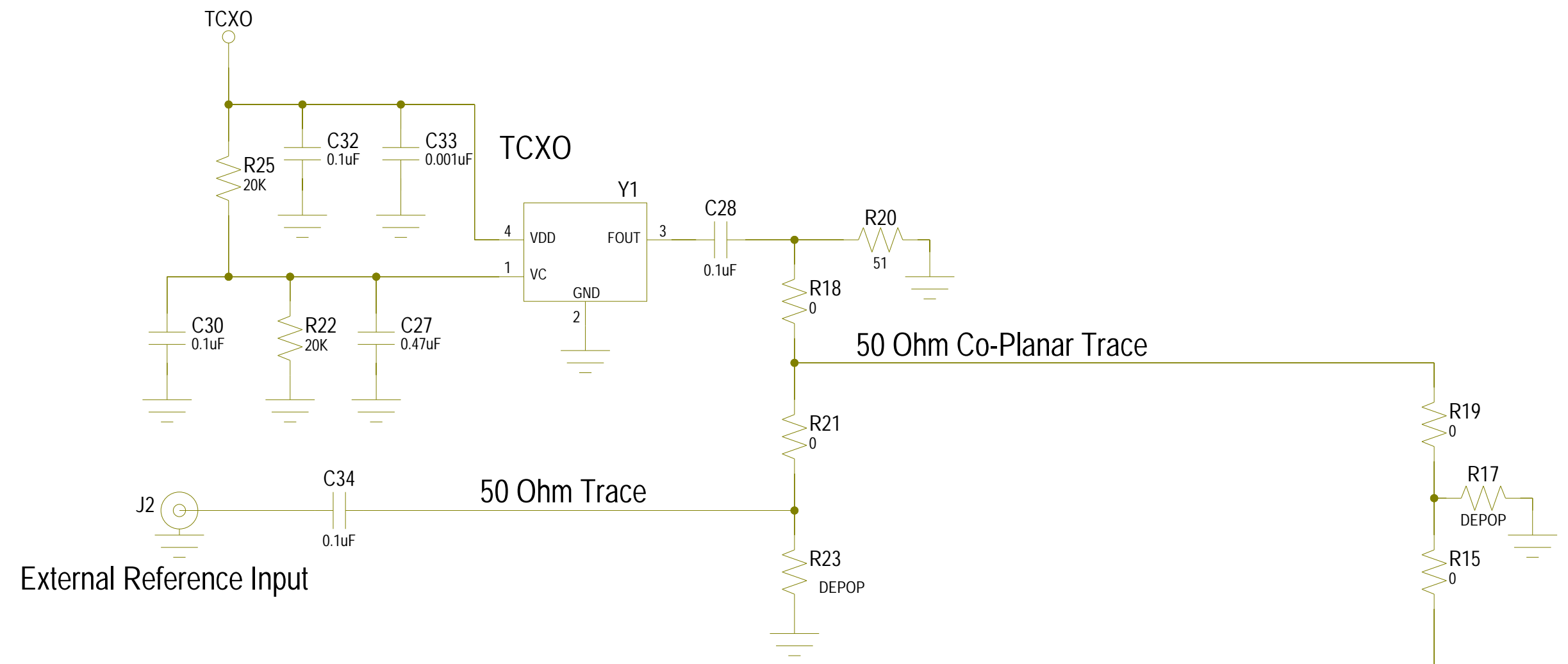
		HITTITE MICROWAVE CORPORATION 20 Alpha Rd Chelmsford, MA 01824	
TITLE SCH, CUSTOMER EVALUATION PLL & VCO FO & FO/2			
PROJECT		SHEET 1 OF 2	
DRAWING #:	129509	CODE ID NO:	1CN88
DRAWN BY:	D.YOUNG	DATE:	11/08/10
		SIZE:	D
		REV:	C



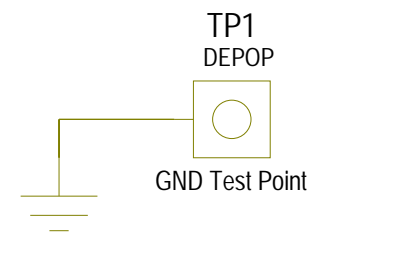
Supply Regulator for PLL+VCO

A recommended design practice is to connect the regulator Enable Pin #3 through a 4.7kOhm resistor to the system microcontroller/FPGA for power management control  
 Small series resistors required in VR1 and VR4 output paths, as shown

REVISIONS					
REV	ECN#	ZONE	DESCRIPTION	NAME	DATE
B	20100127	.....	RELEASED PRODUCTION ECN CP100519	V.VADUVA	12/29/2010
C	CP121275	.....	CHANGE RELEASE PER CP121275	V. VADUVA	09/14/2012

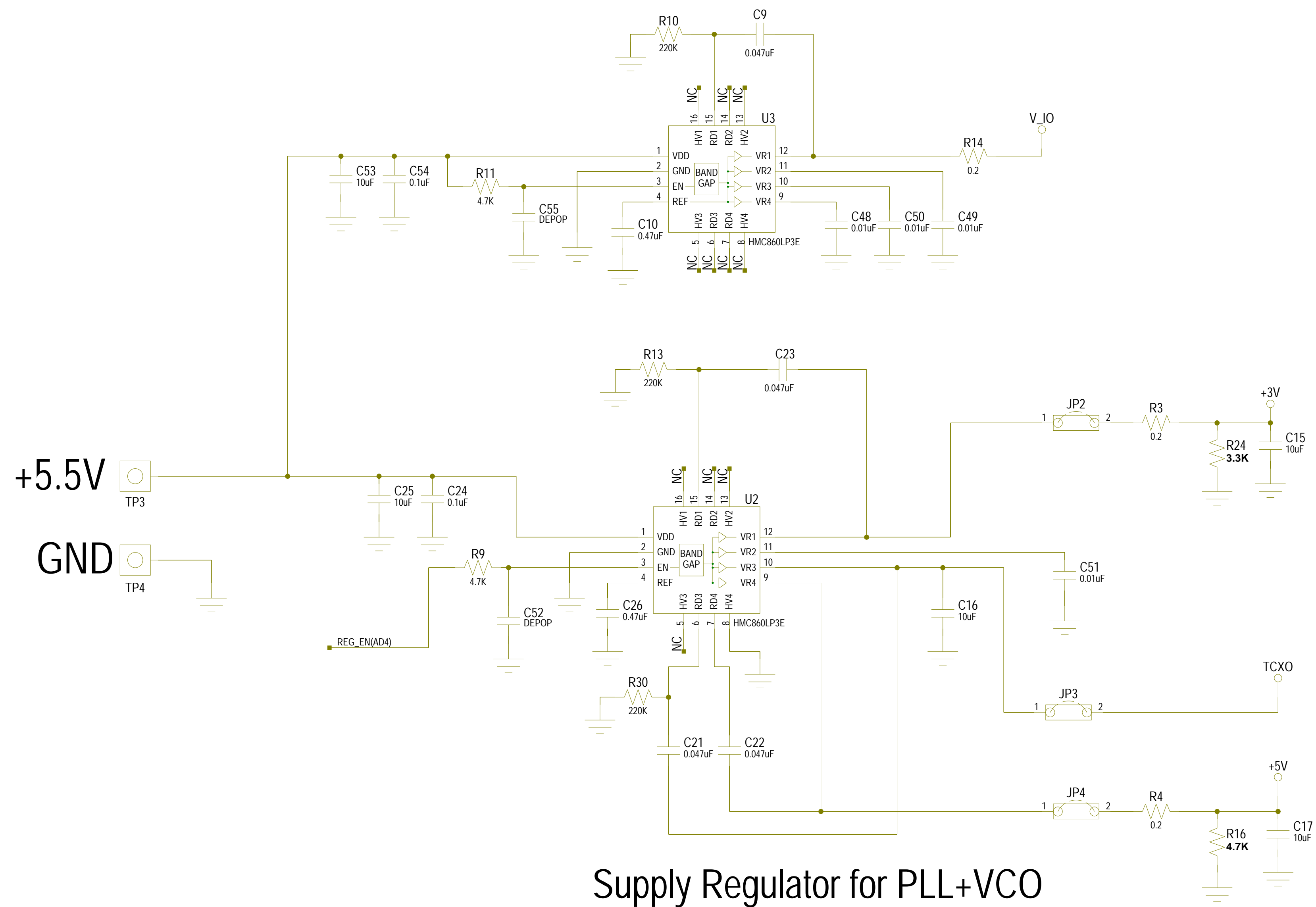


## HMC840 2xFO Integrated PLL & VCO Eval




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HITTITE MICROWAVE CORPORATION 20 Alpha Rd Chelmsford, MA 01824				
TITLE SCH, CUSTOMER EVALUATION PLL & VCO 2 X FO				
PROJECT				
DRAWING #:	129510	SHEET	1 OF 2	
DRAWN BY:	D.YOUNG	DATE:	10/01/2010	REV
			1CN88	C



Supply Regulator for PLL+VCO

A recommended design practice is to connect the regulator Enable Pin #3 through a 4.7kOhm resistor to the system microcontroller/FPGA for power management control  
 Small series resistors required in VR1 and VR4 output paths, as shown

 <b>HITTITE MICROWAVE CORPORATION</b> 20 Alpha Rd Chelmsford, MA 01824	
<b>TITLE</b> SCH, CUSTOMER EVALUATION PLL & VCO 2 X FO	
<b>PROJECT</b> DRAWING #: 129510	
SHEET 2 OF 2 CODE ID NO. 1CN88 SIZE D REV C	
DRAWN BY D.YOUNG	DATE 10/01/2010